

Electronic Acknowledgement Receipt

EFS ID:	1474423
Application Number:	10731566
International Application Number:	
Confirmation Number:	5068
Title of Invention:	Methods and apparatus for transforming sequential logic designs into equivalent combinational logic
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Attorney Docket Number:	6450.P003
Receipt Date:	29-JAN-2007
Filing Date:	08-DEC-2003
Time Stamp:	19:17:18
Application Type:	Utility

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	1019P003US_Coversht.pdf	54959	no	1

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2	Information Disclosure Statement (IDS) Filed	1019P003US_IDS3.pdf	655474	no	4
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